

 INFORMATION DISCLOSURE STATEMENT PTO Form 1449				Docket Number 1305.10US01		Serial Number 09/849,810		
				Applicant(s) SANTIAGO ET AL.				
				Filing Date 05/04/01		Group Art Unit 2661 2665		
U.S. PATENT DOCUMENTS								
EXAMINER INITIALS	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE (IF APPROPRIATE)	
DH		6,175,568	01/16/01	Awdeh	—	—		
DH		6,167,445	12/26/00	Gai et al.	—	—		
DH		6,141,686	10/31/00	Jackowski et al.	—	—		
DH		6,072,989	06/06/00	Witters et al.	—	—		
DH		6,047,002	04/04/00	Hartmann et al.	—	—		
DH		6,046,980	04/04/00	Packer	—	—		
DH		6,046,979	04/04/00	Bauman	—	—		
DH		6,032,190	02/29/00	Bremer et al.	—	—		
DH		5,943,481	08/24/99	Wakeland	—	—		
DH		5,896,383	04/20/99	Wakeland	—	—		
DH		5,828,654	10/27/98	Takase et al.	—	—		
DH		5,764,641	06/09/98	Lin	—	—		
DH		5,666,353	09/09/97	Klausmeier et al.	—	—		
DH		5,598,410	01/28/97	Stone	—	—		
DH		5,566,170	10/15/96	Bakke et al.	—	—		
DH		5,541,920	07/30/96	Angle et al.	—	—		
FOREIGN PATENT DOCUMENTS								
EXAMINER INITIALS	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							YES	NO
OTHER DOCUMENTS								
DH		"EZCHIP Technologies Completes Filing Patent Applications For Its 10/40G Network Processor Core Technology," http://www.ezchip.com/html/press_000918.html , printed January 22, 2001, pp. 1-3						
DH		"7-Layer Packet Processing: A Performance Analysis, White Paper," EZchip, http://www.ezchip.com/html/tech_7layers.html , Retrieved January 22, 2001, pp. 1-8						
DH		"Network Process Designs for Next-Generation Networking Equipment, White Paper," EZchip, http://www.ezchip.com/html/tech_nsppaper.html , Retrieved January 22, 2001, pp. 1-8						
DH		"GILDER TECHNOLOGY REPORT," EZchip, September 2000, http://www.ezchip.com/html/gilder.html , Retrieved January 22, 2001, pp. 1-2						
DH		"Frame Based ATM over SONET/SDH Transport (FAST)," The ATM Forum, Technical Committee, fb-fatm-0151.000, July 2000, 37 pgs.						
DH		Heinanen et al., "A Single Rate Three Color Marker," University of Pennsylvania, September 1999, pp. 1-6						
DH		Heinanen et al., "A Two Rate Three Color Marker," University of Pennsylvania, September 1999, pp. 1-5						
DH		"Putting Routing Tables in Silicon," IEEE NETWORK, Vol. 6, No. 1, January 1992, 11 pgs.						

Examiner:

Duc Ho

Date Considered:

1-19-05